

ON-THE-FLY CONFIGURABLE AUDIO PROCESSING MACHINE

Background of the Invention

1. Field of the Invention

This invention relates generally to audio processing systems and methods, and more particularly to an audio processing machine that is configurable on-the-fly.

2. Description of the Prior Art

Known configurable, fixed-function, high performance audio processing devices are not programmable; but they are configurable enough to accomplish the requisite functions under a rather broad range of assumptions. Such devices, for example, can perform speaker equalization, a predetermined function, by applying downloaded filter coefficients, the configurable element. High performance and low cost of such devices has been achieved by limiting the flexibility. It is therefore important that correct assumptions are made about what functions are necessary, and that all the required hooks be in place to perform those functions. Too much configurability can overcomplicate the audio processing devices, making them more expensive, difficult to use, and more power hungry; while too little configurability can render such devices unusable in some target markets and unable to keep pace with market changes.

In view of the foregoing, a need exists in the marketplace for a high performance audio processing device having significantly increased configurability capabilities and that offers far more precision and throughput than its predecessors without increasing the per-channel cost of the device above the per-channel cost of the predecessors.

Summary of the Invention

The present invention is directed to a high performance audio processing device having significantly increased configurability capabilities and that offers far more precision and throughput than its predecessors without increasing the per-channel cost of the device above the per-channel cost of the predecessors. The device architecture includes a single-cycle, 48X28 bit multiply feature that is configured as a sophisticated switch to minimize the use of branching such that the configurability capabilities of the device are significantly increased. Multiplying by zero shuts a path off, while multiplying by one turns the path on, and multiplying by -1 turns the path on, but with an inversion of the path signal's phase. The use of a full-precision multiply rather than other forms of decision logic also allows "morphing" from one setting to another, and provides new operating states that are linear combinations of the "on" and "off" settings.

According to one aspect of the invention, an audio processing device achieves an unmatched degree of performance with far more configurability at greater size, power and per-channel cost savings than that available from general purpose, fully programmable devices such as digital signal processors (DSPs).

An audio processing machine architecture according to one embodiment comprises:

- a plurality of audio inputs;
- a plurality of audio outputs;
- a plurality of audio filters;
- a plurality of audio processing channels; and
- a plurality of multiply switches configured to selectively mix the plurality of audio inputs and the plurality of audio outputs such that audio signals passing through the plurality of audio inputs are processed via a plurality of audio filters selected from the plurality of audio filters and a plurality of audio processing channels selected from the plurality of audio processing channels to generate at least one desired audio output signal.

Another embodiment of the present invention is an audio processing device comprising:

- means for receiving a plurality of audio input signals;

means for generating a plurality of audio output signals;

means for filtering the plurality of audio input signals to generate a plurality of filtered audio signals;

means for processing the plurality of filtered audio signals to generate a plurality
5 of processed audio signals; and

a plurality of multiply switches configured to selectively mix a plurality of audio
input signals selected from the plurality of audio input signals, a plurality of filtered
audio signals selected from the plurality of filtered audio signals and a plurality of
processed audio signals selected from the plurality of processed audio signals to generate
10 at least one desired audio output signal.

Yet another embodiment of the present invention is an audio processing device
having a plurality of multiply switches operational to selectively mix a plurality of audio
input signals, a plurality of filtered audio signals generated therefrom the plurality of
audio input signals and a plurality of processed audio signals generated therefrom the
15 plurality of filtered audio signals to generate at least one desired audio output signal.

A method of processing an audio signal according to still another embodiment of
the present invention comprises the steps of:

- a. configuring a first plurality of multiply switches, a second plurality of
multiply switches and a third plurality of multiply switches;
- 20 b. mixing a plurality of audio input signals via the first plurality of multiply
switches to generate a plurality of mixed audio signals;
- c. filtering the plurality of mixed audio signals to generate a first plurality of
filtered audio signals;
- d. mixing the first plurality of filtered audio signals via the second plurality
25 of multiply switches to generate to generate a second plurality of filtered audio signals;
- e. processing the second plurality of filtered audio signals to generate a
plurality of processed audio signals; and
- f. mixing the plurality of processed audio signals via the third plurality of
multiply switches to generate at least one desired audio output signal.

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Brief Description of the Drawings

Other aspects, features and advantages of the present invention will be readily appreciated as the invention becomes better understood by reference to the following detailed description when considered in connection with the accompanying drawing figure wherein:

Figure 1 is a simplified block diagram illustrating an audio processing device architecture according to one embodiment;

Figure 2 is a simplified block diagram illustrating the audio processing device depicted in Figure 1, but configured in a two-channel-in, three-channel-out arrangement where the third channel is derived by mixing the two input channels;

Figure 3 is a simplified block diagram illustrating the audio processing device depicted in Figure 1, but with the filters configured in a different sequence that, along with the mixing of left and right channels, effects what is known as a “stereo spreading” algorithm;

Figure 4 is a simplified block diagram illustrating the audio processing device depicted in Figure 1, but with the filters configured according to a *head related transfer function* (HRTF), accomplished in this architecture by changing coefficients, and wherein the mixing and filtering sequence is also configured only by coefficients;

Figure 5 is a simplified block diagram illustrating the audio processing device depicted in Figure 1, but with four channels filtered and downmixed to produce two outputs;

Figure 6 is a simplified block diagram illustrating the audio processing device depicted in Figure 1, but with the filters configured in yet another sequence via different switching paths;

Figure 7 is a simplified block diagram illustrating the audio processing device depicted in Figure 1, but showing a different slice of the overall architecture in greater detail that emphasizes the mixing capability and more particularly illustrates the internal functionality of the audio processing device; and

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While the above-identified drawing figures set forth particular embodiments, other embodiments of the present invention are also contemplated, as noted in the discussion. In all cases, this disclosure presents illustrated embodiments of the present invention by way of representation and not limitation. Numerous other modifications and embodiments can be devised by those skilled in the art which fall within the scope and spirit of the principles of this invention.

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Detailed Description of the Preferred Embodiments

Figure 1 is a simplified block diagram illustrating an audio processing device 10 architecture according to one embodiment. The audio processing device 10 has significantly increased reconfiguration capabilities and offers far more precision and throughput than existing general purpose, fully programmable devices such as DSPs that generally do not focus on a particular type of processing, without increasing the per-channel cost of the device above the per-channel cost of such general purpose devices. The audio processing device 10 architecture includes a single-cycle, 48X28 bit multiply feature that is configured as a sophisticated switch to minimize the use of branching such that the configuration capabilities of the audio processing device 10 are significantly increased. Multiplying by zero shuts a path off, while multiplying by one turns the path on, and multiplying by -1 turns the path on, but with an inversion of the path signal's phase. The present invention is not so limited however, and those skilled in the art will readily appreciate that the audio processing device 10 can also be configured such that different logic levels can be selected as desired or interchanged to implement the above described signal inversion and turning on/off the signal paths. The use of a full-precision multiply rather than other forms of decision logic also allows "morphing" from one setting to another on-the-fly, and provides new operating states that are linear combinations of the "on" and "off" settings. The present inventors found that while the present approach does have some inherent inefficiency, it turns out that this approach is generally more efficient than branching for the typically-encountered tasks. With continued reference to Figure 1, the audio processing device 10 architecture depicts the full multiply/crossbar mix capability available on both the inputs 16-21 and the outputs 22-25, 27 and 29 of the device 10 via multiply switches 11 and their associated crossbar mix 12 as well as multiply switches 13 and their associated crossbar mix 14 respectively. This crossbar mix 12, 14 capability allows mixing and/or reordering of the inputs 16-21 and outputs 22-25, 27 and 29 in any desired manner. Those skilled in the art will therefore readily appreciate that a multitude of other multiply/crossbar mix configurations can be implemented as desired using the audio processing device 10 architecture. Further, multiply switches 26 in other areas of the audio processing device 10 allow the

signal to be routed through various portions of the functionality in various ways. In summary explanation, the internal processor always executes the same code, but, by setting these switches 11, 13, 26 and other coefficients for filters and the like, the outcome changes significantly, as described herein below with reference to Figures 2-8.

5 Figure 2 is a simplified block diagram illustrating the audio processing device 10 depicted in Figure 1, but configured in a two-channel-in, three-channel-out arrangement 30 where the third channel 32 is derived by mixing two input channels 16, 17. It can be appreciated by those skilled in the audio processing art that this implementation is a straightforward application that is easily accomplished with the audio processing device 10 architecture.

Figure 3 is a simplified block diagram illustrating the audio processing device 10 depicted in Figure 1, but with the filters 40 configured in a different sequence that, along with the mixing of left and right channels 42, 44, effects what is known as a “stereo spreading” algorithm. Although the application shown in Figure 3 uses a slightly more 15 complicated structure than that depicted in Figure 2, it can still be derived simply by setting coefficients to modify the results of the same code, using the same overall architecture illustrated in Figure 1. This is a typical application in platforms where the stereo speakers are quite close together, such as in television sets or laptop computers.

Figure 4 is a simplified block diagram illustrating the audio processing device 10 depicted in Figure 1, but with the filters 52 configured according to a *head related transfer function* (HRTF), accomplished in this architecture 50 by changing coefficients, and wherein the mixing and filtering sequence is also configured only by coefficients. The architecture 50 employed via audio processing device 10 assumes that a Dolby AC-3 signal has been decoded and that now it is desired to play those six channels 16-21 20 through a two-channel 42, 44 system. This is accomplished by “virtualizing” the surround speakers associated with inputs 18 and 19 wherein the filters 52 are configured as discussed herein above to give a listener the perception that the sound he/she would have heard from the six speakers of the surround system is present as he/she listens to only two speakers. Again, this is a typically-encountered configuration that must and can 25 be easily and efficiently achievable by the architecture 50 implemented via the audio processing system 10.

Figure 5 is a simplified block diagram illustrating the audio processing device 10 depicted in Figure 1, but with four channels 16-19 filtered and downmixed to produce two outputs 22, 23. This application is similar to that described herein before with reference to Figure 4, but instead assumes a Dolby Pro input audio signal.

5 Figure 6 is a simplified block diagram illustrating the audio processing device 10 depicted in Figure 1, but with the filters configured in yet another sequence via different switching paths. It can be appreciated this application is similar to that described herein before with reference to Figure 3; but instead, the filters are required to be sequenced in a different manner.

10 Figure 7 is a simplified block diagram illustrating the audio processing device 10 depicted in Figure 1, but showing a different slice 60 of the overall architecture in greater detail that emphasizes the mixing capability and more particularly illustrates the internal functionality of the audio processing device 10.

15 Figure 8 is a simplified block diagram illustrating the slice 60 depicted in Figure 7, but configured as a "karaoke mode" architecture 80 to remove whatever is common between the left and right channels 82, 84, to allow mixing in a third channel 86 where a karaoke singer is added in to replace the original vocalist via different switching paths.

In view of the above, it can be seen the present invention presents a significant advancement in the art of audio processing devices and techniques. The applications and architectures described herein before with reference to Figures 2-6 and 8 demonstrate
20 only a small subset of what is possible with the overall architectures illustrated in Figures 1 and 7. It can be appreciated by those skilled in the audio processing art that this degree of flexibility will allow unique coefficient configurations that will enable new applications without the expense of manufacturing new silicon. Further, since this degree
25 of configurability is available by only changing coefficients, and since changing coefficients is easily accomplished using, for example, an IIC bus integrated into the audio processing device 10, new configurations can actually take place on-the-fly in configurable end equipment. The emerging DVD-Audio standard, for example, allows for selections on the same DVD to have different sample rates and different numbers of
30 channels. It can easily be appreciated that the audio processing device 10 architecture can easily handle a wide range of sample rates while also changing on-the-fly to handle

different numbers of input channels and different processing required to meet the DVD-Audio standard. The flexibility afforded by the audio processing device 10 can also be demonstrated by considering another example in which a listener can listen to a CD, then flip a switch and move instantly into karaoke mode where he/she will suppress the lead
5 vocalist and mix in his/her own voice in its place.

Those skilled in the audio processing art will readily recognize that much of the functionality described herein before is already available in other forms. The present architecture, however, avoids the overhead required for branching operations and multiple subroutines stored in a large memory, but is still efficient enough to perform a
10 wide range of functions, has enough precision to perform at audiophile quality, even for 192 kHz sampling rates, and is flexible and configurable enough to change on-the-fly and to allow an extremely wide range of applications without changing the hardware.

This invention has been described in considerable detail in order to provide those skilled in the audio processing art with the information needed to apply the novel
15 principles and to construct and use such specialized components as are required. In view of the foregoing descriptions, it should be apparent that the present invention represents a significant departure from the prior art in construction and operation. However, while particular embodiments of the present invention have been described herein in detail, it is to be understood that various alterations, modifications and substitutions can be made
20 therein without departing in any way from the spirit and scope of the present invention, as defined in the claims which follow.